

<b>Notice of References Cited</b>	Application/Control No. 10/670,018	Applicant(s)/Patent Under Reexamination ZHANG ET AL.	
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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

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	W	Mehta et al. AN FPGA implementation of the graph encoder-decoder for regular LDPC codes, Aug. 2002, www.google.com, pg. 1-18. printed 6-2-2006.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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